

FIG.1 Circuits of HRPD Config. 1

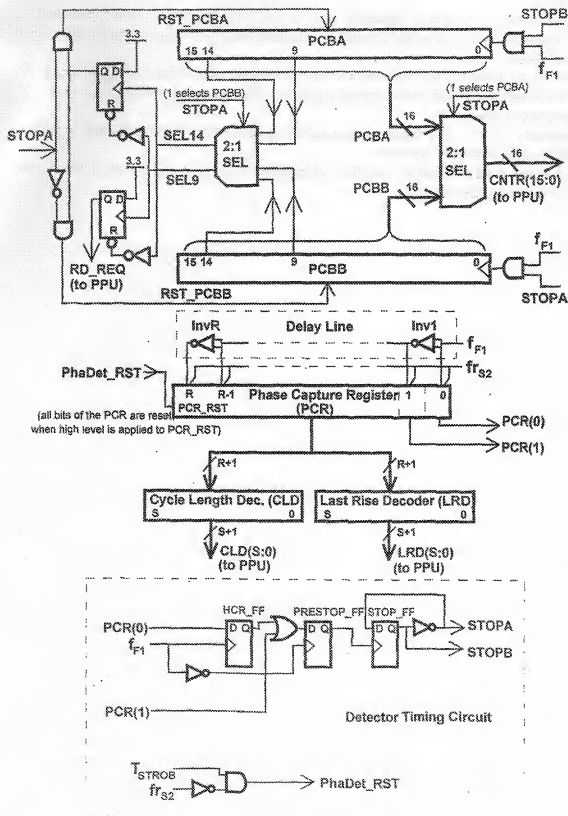
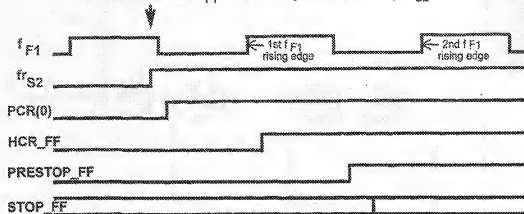


FIG.2 Timing Analysis of HRPD Config.1

For PCR(0)=1:

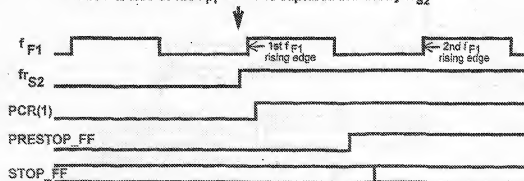
This arrow indicates  $fr_{S2}$  appearance versus  $f_{F1}$  wave.  
The left side of the  $f_{F1}$  wave is captured in PCR by  $fr_{S2}$ .



This arrow indicates  $STOP\_FF$  switching,  
before a second appearance of  $f_{F1}$  rising edge.

For PCR(1)=1:

This arrow indicates  $fr_{S2}$  appearance versus  $f_{F1}$  wave.  
The left side of the  $f_{F1}$  wave is captured in PCR by  $fr_{S2}$ .



This arrow indicates  $STOP\_FF$  switching,  
before a second appearance of  $f_{F1}$  rising edge.



Fig.4 High Resolution Extension of the HRPD Config.3

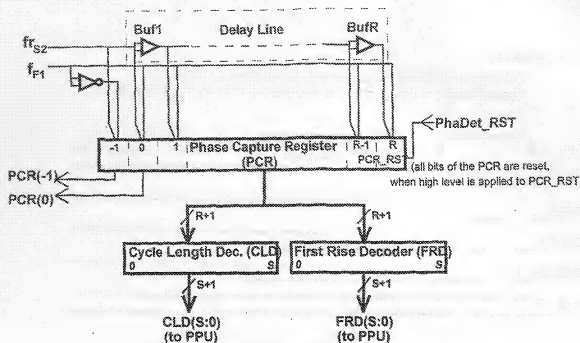


Fig.5 Detector Timing Circuit of the HRPD Config.3

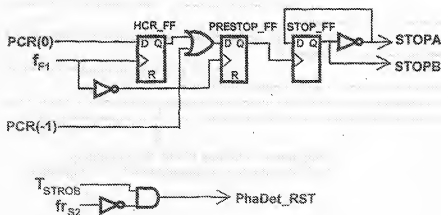
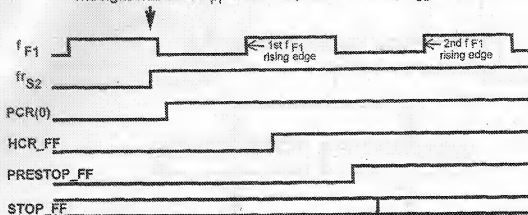


FIG.6 Timing Analysis of the HRPD Config.3

For  $PCR(0)=1$ :

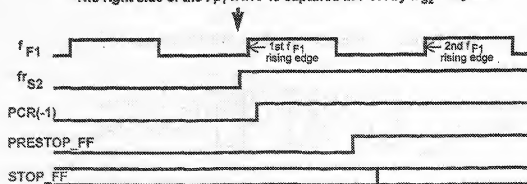
This arrow indicates  $fr_{S2}$  appearance versus  $f_{F1}$  wave.  
The right side of the  $f_{F1}$  wave is captured in PCR by  $fr_{S2}$  delay line.



This arrow indicates  $STOP\_FF$  switching,  
before a second appearance of  $f_{F1}$  rising edge.

For  $PCR(-1)=1$ :

This arrow indicates  $fr_{S2}$  appearance versus  $f_{F1}$  wave.  
The right side of the  $f_{F1}$  wave is captured in PCR by  $fr_{S2}$  delay line.



This arrow indicates  $STOP\_FF$  switching,  
before a second appearance of  $f_{F1}$  rising edge.

Fig.7 High Resolution Extension of the HRPD Config.4

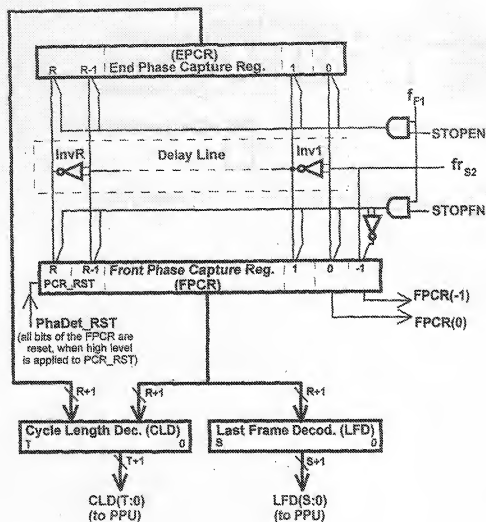


Fig.8 Detector Timing Circuit of the HRPD Config.4

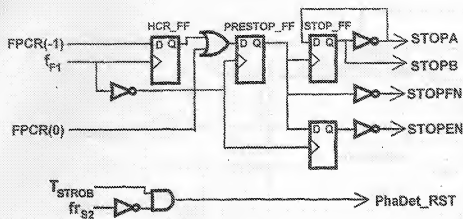


FIG.9 Timing Analysis of the BRPD Config.4

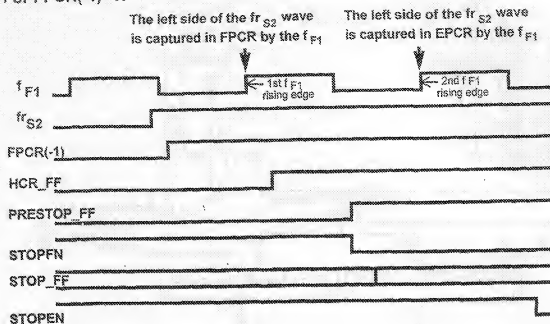
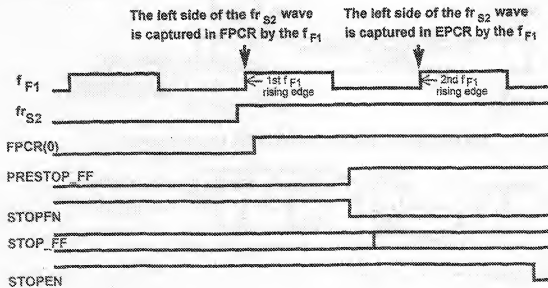
For  $FPCR(-1)=1$ :For  $FPCR(0)=1$ :



Fig.10 High Resolution Extension of the HRPD Config.5

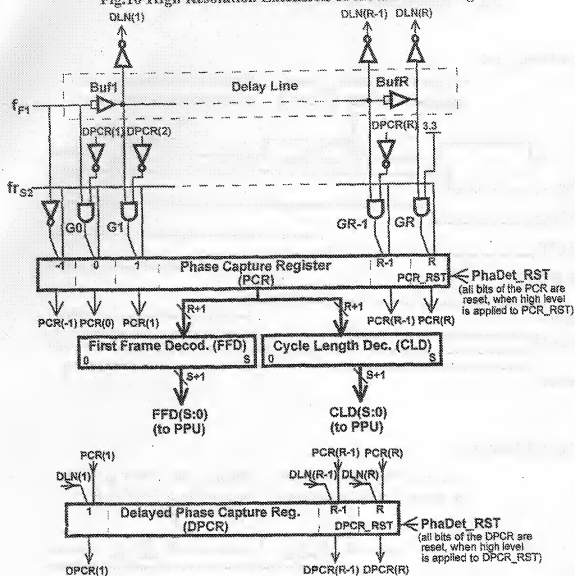


Fig.11 Detector Timing Circuit of the HRPD Config.5

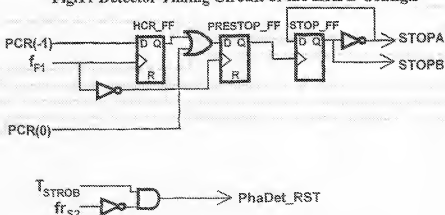


FIG.12 Timing Analysis of the HRPD Config.5

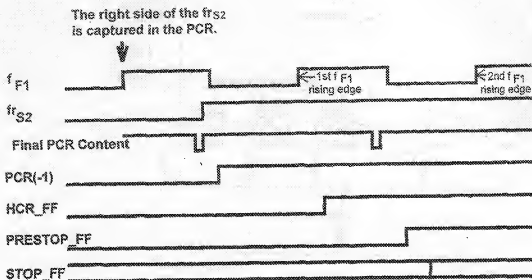
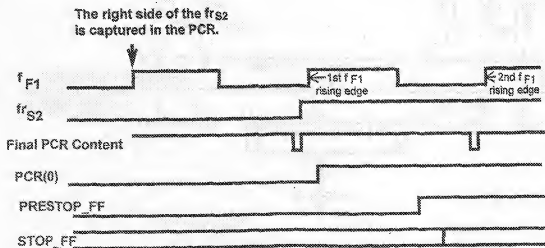
For  $PCR(-1)=1$ :For  $PCR(0)=1$ :

Fig.13 High Resolution Extension of the HRPD Config.6

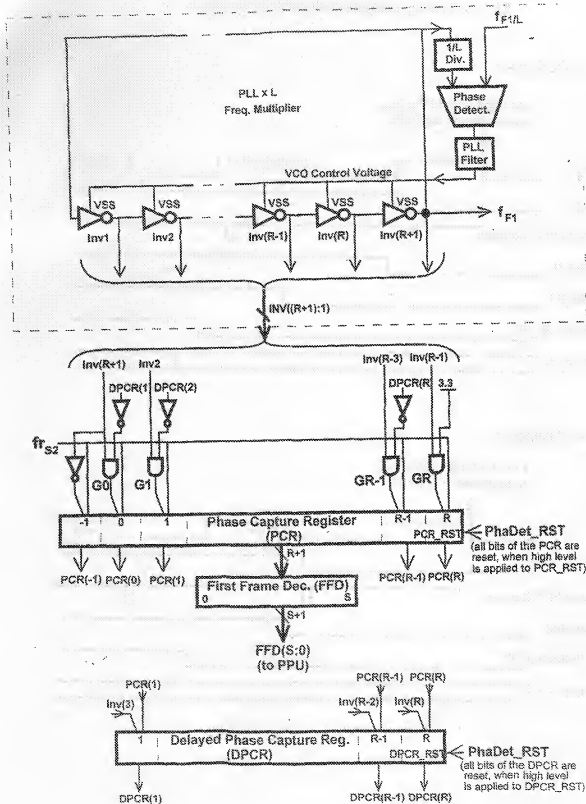
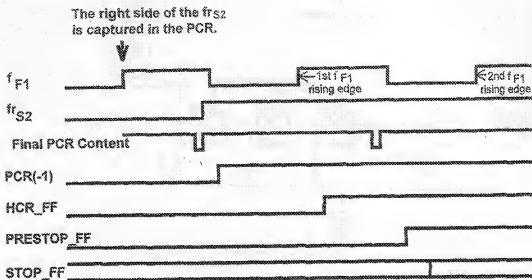


FIG.12 Timing Analysis of the HRPD Config.5

For PCR(-1)=1:



For PCR(0)=1:

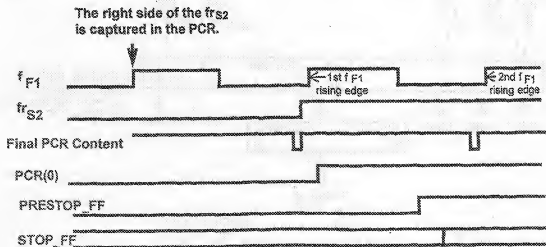
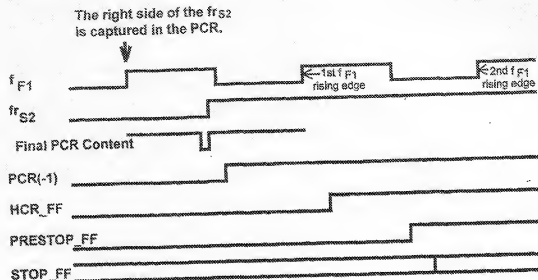
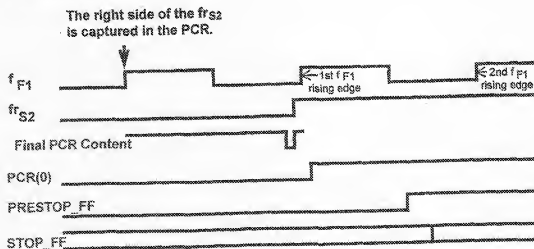


FIG.14 Timing Analysis of the HRPD Config.6

For PCR(-1)=1:



For PCR(0)=1:



## INTERNATIONAL SEARCH REPORT

Int. Appl. No.

PCT/CA 01/00723

A. CLASSIFICATION OF SUBJECT MATTER  
IPC 7 H03D13/00

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H03D H03L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

PAJ, EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5 757 868 A (GURNEY DAVID PAUL ET AL) 26 May 1998 (1998-05-26) abstract; figures 1,2 ----	1
A	EP 0 903 885 A (NIPPON ELECTRIC CO) 24 March 1999 (1999-03-24) figures 1-7 ----	4,6-8,11
A	EP 0 208 449 A (ADVANCED MICRO DEVICES INC) 14 January 1987 (1987-01-14) ----	
A	US 5 491 438 A (MIYAZAKI YUKIO ET AL) 13 February 1996 (1996-02-13) ----- -/-	

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Date of the actual completion of the international search

13 December 2001

Date of mailing of the international search report

27/12/2001

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## INTERNATIONAL SEARCH REPORT

Int. Application No.

PCT/CA 01/00723

## C (Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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A	<p>PATENT ABSTRACTS OF JAPAN vol. 013, no. 325 (E-792). 21 July 1989 (1989-07-21) &amp; JP 01 091519 A (MATSUSHITA ELECTRIC IND CO LTD), 11 April 1989 (1989-04-11) abstract</p>	

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Information on patent family members

Int. Patent Application No.

PCT/CA 01/00723

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